

## AMENDMENTS TO CLAIMS

1. (Currently Amended) A millimeter-wave passive FET switch, comprising a signal line, an FET, an impedance transformation network, wherein:

a gate of said FET is connected with a voltage for controlling the impedance between a drain and a source of said FET,

said drain and said source are series connected with said impedance transformation network, and then parallel connected or series connected with said signal line[[]], ~~and wherein~~

there is no reactance component connected between said drain and said source of said FET, ~~wherein~~

an equivalent impedance of said switch contains no reactance; and

said impedance transformation network is designed to make the off-state effective high capacitance of said FET in high frequency become low impedance, while the on-state low impedance of said FET in high frequency becomes high impedance.

2. (Canceled)

3. (Canceled)

4. (Canceled)